

1 Name & Rank: Faridah M. Ali, Assistant Professor

2 Education

Degree	Discipline	Institution	Year
Ph.D.	Electrical Engineering	Virginia Polytechnic Institute and State University, USA	1988
M.S.	Electrical Engineering	University of Michigan, Ann Arbor, USA	1983
B.S.	Electrical Engineering	Kuwait University	1980

3 Academic Experience

Institution	Designation	Year/Period	FT/PT
Kuwait University	Assistant Professor	Sep88-Sep2005	FT
Kuwait University	Director	July 1995-July1997	PT
Kuwait University	Assistant professor	Sep 2013- present	FT

4 Non-Academic Experience:

Organization	Designation	Year/Period	FT/PT
Private Universities Council, Ministry of Higher Education, Kuwait	Acting Secretary General	Sep2010-Mar2013	FT
Private Universities Council, Ministry of Higher Education	Deputy Secretary General	Sep2005-Sep2010	FT
Kuwait Institute for Scientific Research	Research Assistant	Aug1980-Aug1981	FT

5 Certifications / professional registrations: None

6 Membership in Professional Organization: Kuwait Engineering Society

7 Honors and Awards: None

8 Service Activities:

- Coordinator of Digital Systems Laboratory (Sep 2021-Present)
- Member of Computer Architecture TAG group (Sep 2013 - present)
- Member of Computer Engineering Undergraduate Program Committee (Sep 2013 - 2014)
- Member of Strategic Planning Committee (Sep 16- June2017)
- Coordinator for Embedded Systems Laboratory (Sep 2013 – July 2015)

9 Publications and Presentations (from past five years):

- F. M. Ali, H.AI-Hamadi, A.Ghoniem and H.D.Sherali, "Hardware-software co-design for reconfigurable field programmable gate arrays using mixed-integer programming", *Informatica*, 36 (2012) 287-295.
- I.Ahmad, F.M.Ali and A.S.Das, "Synthesis of finite state machines for improved state verification", *Computers and Electrical Engineering*, 32 (2006) 349-363.
- I.Ahmad, F.M.Ali and A.S.Das, "LANG-algorithm for constructing unique input/output sequences in finite state machines", *IEE Proceedings-Computers and Digital Techniques*, 151 (2004) 131-140.
- F.M.Ali and A.S.Das, "Hardware-software co-synthesis of hard real-time systems with reconfigurable FPGAs", *Computers and Electrical Engineering*, 30 (2004) 471-489.

10 Recent Professional Development Activities: None